Bi-Directional Digital I/O Board for PCI DIO-48D2-PCI



* Specifications, color and design of the products are subject to change without notice.

Features

This board can be used to input/output 48 points bi-directional digital corresponding to the equivalence to the i8255 mode 0.

This board has up to 48 unisolated TTL-level input/output channels whose response speed is 2000 sec that is powered by the equivalence to the mode 0 of i8255 device for general-purpose. You can select the input/output by the application software in eight signals units (in four signals unit for some inputs/outputs).

You can use up to 48 channels of the input signals as interrupt events.

You can use up to 48channels of the input signals as interrupt events and also disable or enable the interrupt in bit units and select the edge of input signals, at which to generate an interrupt.

This product has a digital filter function to prevent wrong recognition of input signals from carrying noise or a chattering.

This product has a digital filter function to prevent wrong recognition of input signals by noise or chattering is provided. All input terminals can be added a digital filter, and the setting can be performed by software.

Windows/Linux compatible driver libraries are supported.

Using the digital I/O driver makes it possible to create applications of Windows/Linux. In addition, a diagnostic program by which the operations of hardware can be checked is provided.

Connectors are compatible with PCI compatible board PIO-48D(PCI).

There is compatibility in terms of connector shape and pin assignments with PCI compatible board PIO-48D(PCI), it is easy to migrate from the existing system.

LabVIEW is supported by a plug-in of dedicated library VI-DAQ.

Using the dedicated library VI-DAQ makes it possible to create each application for LabVIEW.

This product is a PCI bus-compliant interface board that extends the input/output function of

bi-directional digital signal. This board has up to 48 unisolated TTLlevel input/output channels that is powered by the equivalence to the mode 0 of i8255 chips, and you can use up to 48 channels of the input signals as interrupt inputs. You can select the input/output by the application software in eight signals units (in four signals unit for some inputs/outputs). Additionally, the digital filter function is equipped with this product. Windows/Linux driver is supported.

Using the dedicated library VI-DAQ makes it possible to create each application for LabVIEW.

- * The contents in this document are subject to change without notice.
- * Visit the CONTEC website to check the latest details in the document.
- * The information in the data sheets is as of September, 2022.

Specifications

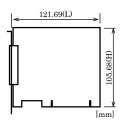
	ltem	Specification			
I/O					
	I/O format	Unisolated TTL-level input (Positive logic)			
I	Number of I/O channels	48 channels (all available for interrupts)			
Pull-up resistance		10kΩ			
		48 interrupt input signals are arranged into a single output of interrupt signal INTA An interrupt is generated at the rising edge (LOW-to-HIGH transition).			
Ī	Response time	Within 200nsec			
Ī	Rated output current	I _{OL} =24mA (Max.) I _{OH} =-15mA (Max.)			
Com	Common				
	I/O address	Any 32-byte boundary (Common to I/O part)			
Ī	Power consumption (Max)	5VDC 600mA			
	Operating condition	0 - 50°C, 10 - 90%RH (No condensation)			
	Allowable distance of signal extension	Approx. 1.5m (depending on wiring environment)			
	Bus specification	PCI (32bit, 33MHz, Universal key shapes supported *2)			
	Dimension (mm)	121.69(L) x 105.68(H) *3			
	Connector	•			
	CN1	96 pin half pitch connector [M (male) type] PCR-E96LMD+ [mfd. by HONDA TSUSHIN KOGYO CO., LTD.] or equivalent to it			
CN2,3 50 pin IC pitch pin header connector PS-50PE-D4T1-B1A [mfd.by JAE] or equivalent to					
	Weight	130g			
	Standard	VCCI Class A, CE Marking (EMC Directive Class A, RoHS Directive), UKCA			

*1 Data "0" and "1" correspond to the High and Low levels, respectively.

*2 This board requires power supply at +5V from an expansion slot (it does not work on a machine with a +3.3V power supply alone).

*3 Board No.7388, for the board of the 7388A is the 176.41 (L) × 106.68 (H).

Board Dimensions



The standard outside dimension (L) is the distance from the end of the board to the outer surface of the slot cover. [mm]

Support Software

Windows version of digital I/O driver API-DIO(WDM)

The API-DIO(WDM) is the Windows version driver library software that provides products in the form of Win32 API functions (DLL). Various sample programs such as Visual Basic and Visual C++, etc and diagnostic program useful for checking operation is provided.

For more details on the supported OS, applicable language and how to download the updated version, please visit the CONTEC's Web site.

Linux version of digital I/O driver API-DIO(LNX)

The API-DIO(LNX) is the Linux version driver software which provides device drivers (modules) by shared library and kernel version. Various sample programs of gcc are provided.

For more details on the supported OS, applicable language and how to download the updated version, please visit the CONTEC's Web site.

Data acquisition VI library for LabVIEW VI-DAQ

This is a VI library to use in National Instruments LabVIEW. VI-DAQ is created with a function form similar to that of LabVIEW's Data Acquisition VI, allowing you to use various devices without complicated settings.

Cable & Connector

Cable (Option)

Shield Cable with 96-Pin Half-Pitch Connectors at Both Ends : PCB96PS-0.5P (0.5m), PCB96PS-1.5P (1.5m)

Flat Cable with 96-Pin Half-Pitch Connectors at Both Ends : PCB96P-1.5 (1.5m)

Shield Cable with 96-Pin Half-Pitch Connectors at One End : PCA96PS-0.5P (0.5m), PCA96PS-1.5P (1.5m)

Flat Cable with 96-Pin Half-Pitch Connectors at One End : PCA96P-1.5 (1.5m)

Accessories

Accessories (Option) Screw Terminal	: EPD-96A *1*2
Screw Terminal	: EPD-96 *1
Digital I/O 64CH Series Terminal Panel	: DTP-64A *1

*1 A PCB96P or PCB96PS optional cable is required separately.

- *2 "Spring-up" type terminal is used to prevent terminal screws from falling off.
- * Check the CONTEC's Web site for more information on these options.

Packing List

Board [DIO-48D2-PCI] ...1 Setup guide ... 1 Product Registration Card & Warranty Certificate ...1 Serial number label ...1

Connector Pin Assignment

Pin Assignments of Interface Connector (CN1)					
2-C port (High)	GND B45 2-PC5 B44	[1] A48 A47 A46 A45 A44	- 1-PC7 GND - 1-PC6 GND - 1-PC5	1-C port (High)	
2-C port (Low)	GND B43 2-PC4 B42 GND B41 2-PC3 B40 GND B39 2-PC2 B38 GND B37 2-PC1 B36	A43	GND 1-PC4 GND 1-PC3 GND 1-PC2 GND 1-PC2 	1-C port (Low)	
	GND B35 2-PCO B34 GND B33 2-PB7 B33 GND B31 2-PB6 B30 GND B29 2-PB5 B28 GND B27	A35	GND 1-PC0 GND 1-PB7 GND 1-PB6 GND 1-PB6 T1-PB5 GND		
2-B port	2-PB4 B26 GND B25 2-PB3 B24 GND B23 2-PB2 B22 GND B21 2-PB1 B20 GND B18	A26 A25 A24 A23 A22 A21 A20 A19 A18	"1-PB4 "GND "1-PB3 "GND "1-PB2 GND 1-PB1 GND 1-PB0	1-B port	
2-A port	GND B17 GND B17 2-PA7 B16 GND B15 2-PA6 B14 GND B13 2-PA5 B12 GND B11 2-PA5 B10	A17 ··· A16 ·· A15 ··· A14 ·· A13 ·· A12 ·· A11 ·· A10 ··	GND 1-PA7 GND 1-PA6 GND 1-PA5 GND 1-PA4	1-A port	
	GND *** B09 2-PA3 *** B08 GND *** B07 2-PA2 *** B06 GND *** B05 2-PA1 *** B04 GND *** B03 2-PA0 *** B02 GND *** B01 [96]	A09 " A08 " A07 " A06 " A05 " A04 " A03 " A02 " A01 "	GND 1-PA3 GND 1-PA2 GND 1-PA1 GND 1-PA0 GND		
	[96]	[48]	J		

Pin Assignments of Interface Connector (CN2, CN3)

5	CN	2	
1-C port (High) 1-PC7 - 1-PC6 - 1-PC5 - 1-PC4 -	- A01 - A02 - A03 - A04	B01 B02 B03 B04	GND GND GND GND
1-C port (Low) 1-PC3	- A05 - A06 - A07 - A08 - A09	B05 - B06 - B07 - B08 - B09 -	- GND - GND - GND - GND - GND
1-B port 1-B port 1-PB5	- A10 - A11 - A12 - A13 - A14	B10 B11 B12 B13 B14	GND GND GND GND GND
1-PB1	A15 A16	B15 - B16 - B17 -	- GND - GND
1-A port 1-PA6 - 1-PA6 - 1-PA6 - 1-PA5 - 1-PA3 - 1-PA3 - 1-PA2 - 1-PA2 - 1-PA0 - - 1-PA0 - - - - - - - - - - - - - -	A17 A18 A19 A20 A21 A22 A23 A24 A25	B17 B18 B19 B20 B21 B22 B23 B24 B25	GND GND GND GND GND GND GND GND GND

$ \begin{array}{c} 2 \cdot \operatorname{C} \text{ port} \\ (\text{High}) \\ 2 \cdot \operatorname{PC6} & - \cdot & \operatorname{A01} & \text{B01} & - \cdot \cdot \operatorname{GN} \\ 2 \cdot \operatorname{PC6} & - \cdot & \operatorname{A02} & \text{B02} & - \cdot \operatorname{GN} \\ 2 \cdot \operatorname{PC5} & - \cdot & \operatorname{A03} & \text{B03} & - \cdot \operatorname{GN} \\ 2 \cdot \operatorname{PC5} & - \cdot & \operatorname{A03} & \text{B03} & - \cdot \operatorname{GN} \\ 2 \cdot \operatorname{PC4} & - \cdot & \operatorname{A04} & \text{B04} & - \cdot \operatorname{GN} \\ 2 \cdot \operatorname{PC2} & - \cdot & \operatorname{A05} & \text{B05} & - \cdot \operatorname{GN} \\ 2 \cdot \operatorname{PC2} & - \cdot & \operatorname{A05} & \text{B06} & - \cdot \operatorname{GN} \\ 2 \cdot \operatorname{PC2} & - \cdot & \operatorname{A06} & \text{B06} & - \cdot \operatorname{GN} \\ 2 \cdot \operatorname{PC2} & - \cdot & \operatorname{A08} & \text{B08} & - \cdot \operatorname{GN} \\ 2 \cdot \operatorname{PC0} & - \cdot & \operatorname{A08} & \text{B08} & - \cdot \operatorname{GN} \\ 2 \cdot \operatorname{PC0} & - \cdot & \operatorname{A08} & \text{B08} & - \cdot \operatorname{GN} \\ 2 \cdot \operatorname{PB7} & - \cdot & \operatorname{A08} & \text{B09} & - \cdot \operatorname{GN} \\ 2 \cdot \operatorname{PB6} & - \cdot & \operatorname{A11} & \operatorname{B11} & - \cdot \operatorname{GN} \\ 2 \cdot \operatorname{PB5} & - \cdot & \operatorname{A11} & \operatorname{B11} & - \cdot \operatorname{GN} \\ 2 \cdot \operatorname{PB3} & - \cdot & \operatorname{A12} & \operatorname{B12} & - \cdot \operatorname{GN} \end{array} $	
$\begin{array}{c c} 2^{\circ}\mathrm{C} \mbox{ port } \\ 2^{\circ}\mathrm{C} \mbox{ port } \\ (\mathrm{Low}) \\ & 2^{\circ}\mathrm{PC1} \\ & - \mathrm{A07} \\ & \mathrm{B07} \\ & - \mathrm{GN} \\ & 2^{\circ}\mathrm{PC1} \\ & - \mathrm{A07} \\ & \mathrm{B08} \\ & - \mathrm{GN} \\ & 2^{\circ}\mathrm{PB7} \\ & \mathrm{A08} \\ & \mathrm{B08} \\ & - \mathrm{GN} \\ & 2^{\circ}\mathrm{PB7} \\ & - \mathrm{A08} \\ & \mathrm{B09} \\ & - \mathrm{GN} \\ & - \mathrm{GN} \\ & 2^{\circ}\mathrm{PB5} \\ & - \mathrm{A11} \\ & \mathrm{B11} \\ & - \mathrm{GN} \\ & 2^{\circ}\mathrm{PB3} \\ & - \mathrm{A12} \\ & \mathrm{B12} \\ & - \mathrm{GN} \\ & \mathrm{S1} \\ & - \mathrm{GN} \\ \end{array}$	D
$\begin{array}{c} 2\text{-}PB6 & & A10 & B10 & & GN \\ 2\text{-}PB5 & & A11 & B11 & & GN \\ 2\text{-}PB4 & & A12 & B12 & & GN \\ 2\text{-}PB3 & & A13 & B13 & & GN \end{array}$	D
2-PB2 - A14 B14 - GN 2-PB1 - A15 B15 - GN 2-PB0 - A16 B16 - GN	
$\begin{array}{c} 2 \cdot PA7 & A17 & B17 &GN \\ 2 \cdot PA6 & +-A18 & B18 &GN \\ 2 \cdot PA5 & -+A19 & B19 & -GN \\ 2 \cdot PA3 & +-A19 & B19 & -GN \\ 2 \cdot PA3 & +-A20 & B20 &GN \\ 2 \cdot PA3 & +-A21 & B21 &GN \\ 2 \cdot PA3 & +-A22 & B22 & -GN \\ 2 \cdot PA1 & +-A23 & B23 &GN \\ 2 \cdot PA1 & +-A23 & B23 &GN \\ 2 \cdot PA0 & +-A24 & B24 &GN \\ +5VDC & Vcc & +-A25 & B25 &GN \\ \end{array}$	

CN3

Differences between DIO-48D2-PCI and PIO-48D(PCI)

The DIO-48D2-PCI is connector-pin compatible with the conventional PIO-48D(PCI) but has the following differences from it:

	PIO-48D(PCI)	DIO-48D2-PCI Board No.: 7388, 7388A	DIO-48D2-PCI Board No.: 7388B or later	
Digital filter function	I filter function None		Have	
Selecting the Interrupt Edge	None	Have		
Dimension (mm)	176.41(L) x 106.68(H)		121.69(L) x 105.68(H)	