Digital I/O Board with Opto-Isolation for PCI
PIO-64/64L(PCI)H

This product is a PCI bus-compliant interface board for input/output of digital signals.   This product can input and output digital signals at 12 - 24VDC.

PIO-64/64L(PCI)H features 64 opto-coupler isolated inputs (for current sinking output) and 64 opto-coupler isolated open-collector outputs (current sinking type).   You can use 16 of its input signals as interrupt request signals. In addition, the digital filter function to prevent wrong recognition of input signals is provided and output transistor protection circuit (surge voltage protection and overcurrent protection).

Windows/Linux device driver is supported with this product.

* The contents in this document are subject to change without notice.
* Visit the CONTEC website to check the latest details in the document.
* The information in the data sheets is as of September, 2023.

* Specifications, color and design of the products are subject to change without notice.

Features

Opto-coupler isolated input (supporting current sink output) and opto-coupler isolated open-collector output (current sink type)
PIO-64/64L(PCI)H has the 64ch of opto-coupler isolated input (supporting current sink output) and 64ch of opto-coupler isolated open-collector output (current sink type) whose response time is 200μsec.   Common terminal provided per 16channels, capable of supporting a different external power supply.   Supporting driver voltages of 12 - 24 VDC for I/O.

Opto-coupler bus isolation
As the PCI bus (PC) is isolated from the input and output interfaces by opto-couplers, this product has excellent noise performance.

16 input signals can be used as interrupt request signals
You can use 16 of its input signals as interrupt request signals and also disable or enable the interrupt in bit units and select the edge of the input signals, at which to generate an interrupt.

Windows/Linux support device driver
Using the device driver API-TOOL makes it possible to create applications of Windows/Linux. In addition, a diagnostic program by which the operations of hardware can be checked is provided.

Equipped with digital filter to prevent wrong recognition of input signals from carrying noise or a chattering
This product has a digital filter to prevent wrong recognition of input signals from carrying noise or a chattering. All input terminals can be added a digital filter, and the setting can be performed by software.
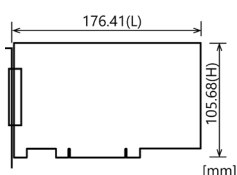
Zener diode for surge voltage protection and the circuit for overcurrent protection
Zener diodes are connected to the output circuits to protect against surge voltages.   In addition, the output circuit, it attaches the overcurrent protection circuit at the output 8-channel unit.   The output rating is max. 35VDC, 100mA per channel.

Included Items

Product [PIO-64/64L(PCI)H] ...1
Please read the following ... 1

Physical Dimensions

The standard outside dimension (L) is the distance from the end of the board to the outer surface of the slot cover.

Specifications

Function specification

Item		Specifications
Input	Type	Opto-Isolated Input (for current sinking output) (Negative logic *1)
	Number of Channels	64ch (16 channels for interrupts) (One common per 16 channels)
	Input resistance	4.7kΩ
	Current required to turn ON	2.0mA or more
	Current required to turn OFF	0.16mA or less
	Interrupts	Combine 16 interrupt signals to one interrupt request signal as the INTA. Either rising edge or falling edge of input signal can generate interrupt.
	Response time	200μsec within
Output	Type	Opto-Isolated Open Collector Output (current sinking type) (Negative logic *1)
	Number of Channels	64ch (One common power supply per 16 channels)
	Output rated voltage	35VDC (Max.)
	Output rated current	100mA/channel (Max.)
	Residual voltage with output on	0.5V or less (Output current ≤ 50mA), 1.0V or less (Output current ≤ 100mA)
	Surge protector	Zener diode RD47FM(Renesas) or equivalent
	Response time	200μsec within
Common	Connecting distance	50m(Typ.) (depending on wiring environment)
	I/O address	Any 32-byte boundary
	Interruption level	1 level use
	Boards in one system	Maximum of 16 boards can be install in a same system.
	Isolated voltage between channels	250Vrms
	Isolated voltage (I/O-to-system) *2	2500Vrms
	External circuit power supply	12 - 24VDC(±10%)
	Power consumption	5VDC 500mA (Max.)
	PCI bus specification	32bit, 33MHz, Universal key shapes supported *3
	Dimension (mm)	176.41(L) x 105.68(H) *4
	Weight	215g

*1   Data "0" and "1" correspond to the High and Low levels, respectively.

*2   There was no electrical breakdown after having impressed the specification voltage with AC50/60Hz and DC for one minute to the insulation part.

*3   This product requires power supply at +5V from an expansion slot (it does not work on a machine with a +3.3V power supply alone).

*4   The size of board No.7299 is 176.41 (L) x 106.68 (H) mm.

Installation Environment Requirements

Item	Specifications
Operating ambient temperature	0 - +50℃
Operating ambient humidity	10 - 90%RH (No condensation)
Floating dust particles	Not to be excessive
Corrosive gases	None
Standard	VCCI Class A, CE Marking (EMC Directive Class A, RoHS Directive), UKCA

Support Software

Name	Contents	How to get
Windows Version Digital I/O Driver software API-DIO(WDM)	The Windows device driver is provided as a form of Windows API functions. Various sample programs such as C# and Visual Basic .NET , Visual C++, Python etc. and diagnostic program useful for checking operation is provided.	Download from the CONTEC website *1
Linux Version Digital I/O Driver software API-DIO(LNX)	The Linux device driver is provided as a shared library. The software includes various sample programs such as gcc (C, C++) and Python programs, as well as a configuration tool to configure the device settings.	Download from the CONTEC website *1
Software Development Tool Kits (SDK) and Support Software	In addition to the device drivers, we offer many software programs for using CONTEC devices in an easier manner.	Download from the CONTEC website *2

*1 Download the files from the following URL.

   https://www.contec.com/download/

*2 For supported software, search the CONTEC website for this product and view the product page.

   https://www.contec.com/

Optional Products

Product Name	Model type	Description
Shielded Cable With Two 100pin Connector	PCB100PS-0.5	0.5m
	PCB100PS-1.5	1.5m
	PCB100PS-3	3m
	PCB100PS-5	5m
Connection Conversion Shield Cable (100P→96P)	PCB100/96PS-1.5	1.5m
	PCB100/96PS-3	3m
	PCB100/96PS-5	5m
Flat Cable with One 100-Pin Connector	PCA100P-1.5	1.5m
	PCA100P-3	3m
Connection Conversion Shield Cable (100pin→37pin D-SUB x 2)	PCB100WS-1.5	1.5m
	PCB100WS-3	3m
	PCB100WS-5	5m
Screw Terminal (M3 * 100)	EPD-100A	*1 *2 *5
Screw Terminal (M3 * 96)	EPD-96A	*1 *3 *5
Terminal Unit for Relay Terminal Banks	EPD-96	*3 *5
Screw Terminal (M3 * 37P)	EPD-37A	*1 *4 *6
Screw Terminal (M3.5 * 37)	EPD-37	*4 *6
Screw Terminal	DTP-64A	*3 *5
General Purpose Terminal	DTP-3C	*4 *6
Screw Terminal	DTP-4C	*4 *6
Signal monitor Accessory for Digital I/O (64bits)	CM-64L	*3 *5
Connector Conversion Board (96pin→37pinx2)	CCB-96	*3 *5

*1   "Spring-up" type terminal is used to prevent terminal screws from falling off.

*2   PCB100PS optional cable is required separately.

*3   PCB100/96PS optional cable is required separately.

*4   PCB100WS optional cable is required separately.

*5   If using both the CNA and CNB connectors, two each of the terminal block and cable sets are required.

*6   If using both the CNA and CNB connectors, two cable sets are required.
     You will also require sufficient terminal blocks for the number of I/O points you are using.

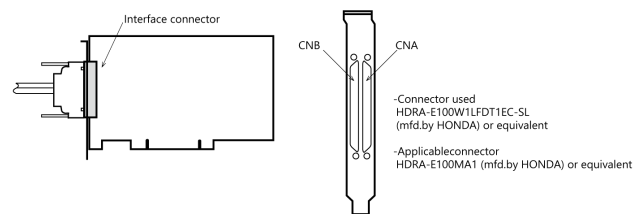*   Information about the option products, see the Contec's website.

Component Name
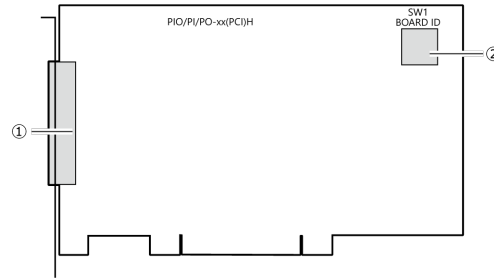
No.	Name
1	Interface Connector
2	Board ID Setting Switch

Connecting an Interface Connector

To connect an external device to this product, plug the cable from the device into the interface connector (CNA, CNB) shown below.

-Connector used HDRA-E100W1LFDT1EC-SL (mfd.by HONDA) or equivalent

-Applicableconnector HDRA-E100MA1 (mfd.by HONDA) or equivalent

Layout on the Interface Connector(CNA, CNB)

CNB

Left group	Signal	Pin	Pin	Signal	Right group
Common plus pin for +E/+F output ports	P-E/F	100	50	P-A/B	Common plus pin for +A/+B output ports
	P-E/F	99	49	P-A/B	
+F port (Output)	O-F7	98	48	O-B7	+B port (Output)
	O-F6	97	47	O-B6	
	O-F5	96	46	O-B5	
	O-F4	95	45	O-B4	
	O-F3	94	44	O-B3	
	O-F2	93	43	O-B2	
	O-F1	92	42	O-B1	
	O-F0	91	41	O-B0	
+E port (Output)	O-E7	90	40	O-A7	+A port (Output)
	O-E6	89	39	O-A6	
	O-E5	88	38	O-A5	
	O-E4	87	37	O-A4	
	O-E3	86	36	O-A3	
	O-E2	85	35	O-A2	
	O-E1	84	34	O-A1	
	O-E0	83	33	O-A0	
Common minus pin for +E/+F output ports	N-E/F	82	32	N-A/B	Common minus pin for +A/+B output ports
	N-E/F	81	31	N-A/B	
	N-E/F	80	30	N-A/B	
	N-E/F	79	29	N-A/B	
	N-E/F	78	28	N-A/B	
	N-E/F	77	27	N-A/B	
	N.C.	76	26	N.C.	
	N.C.	75	25	N.C.	
Common plus pin for +C/+D output ports	P-C/D	74	24	P-8/9	Common plus pin for +8/+9 output ports
	P-C/D	73	23	P-8/9	
+D port (Output)	O-D7	72	22	O-97	+9 port (Output)
	O-D6	71	21	O-96	
	O-D5	70	20	O-95	
	O-D4	69	19	O-94	
	O-D3	68	18	O-93	
	O-D2	67	17	O-92	
	O-D1	66	16	O-91	
	O-D0	65	15	O-90	
+C port	O-C7	64	14	O-87	+8 port

CNA

Left group	Signal	Pin	Pin	Signal	Right group
	N.C.	1	51	N.C.	
	N.C.	2	52	N.C.	
	N.C.	3	53	N.C.	
	N.C.	4	54	N.C.	
	N.C.	5	55	N.C.	
	N.C.	6	56	N.C.	
+0 port (Input)	I-00	7	57	I-40	+4 port (Input)
	I-01	8	58	I-41	
	I-02	9	59	I-42	
	I-03	10	60	I-43	
	I-04	11	61	I-44	
	I-05	12	62	I-45	
	I-06	13	63	I-46	
	I-07	14	64	I-47	
+1 port (Input)	I-10	15	65	I-50	+5 port (Input)
	I-11	16	66	I-51	
	I-12	17	67	I-52	
	I-13	18	68	I-53	
	I-14	19	69	I-54	
	I-15	20	70	I-55	
	I-16	21	71	I-56	
	I-17	22	72	I-57	
Common plus pin for +0/+1 input ports	P-0/1	23	73	P-4/5	Common plus pin for +4/+5 input ports
	P-0/1	24	74	P-4/5	
	N.C.	25	75	N.C.	
	N.C.	26	76	N.C.	
	N.C.	27	77	N.C.	
	N.C.	28	78	N.C.	
	N.C.	29	79	N.C.	
	N.C.	30	80	N.C.	
	N.C.	31	81	N.C.	
	N.C.	32	82	N.C.	
+2 port (Input)	I-20	33	83	I-60	+6 port (Input)
	I-21	34	84	I-61	
	I-22	35	85	I-62	
	I-23	36	86	I-63	
	I-24	37	87	I-64	

(Output)						(Output)						+3 port (Input)						+7 port (Input)
O-C6	63			13	O-86			I-25	38			88	I-65		
O-C5	62			12	O-85			I-26	39			89	I-66		
O-C4	61			11	O-84			I-27	40			90	I-67		
O-C3	60			10	O-83			I-30	41			91	I-70		
O-C2	59			9	O-82			I-31	42			92	I-71		
O-C1	58			8	O-81			I-32	43			93	I-72		
O-C0	57			7	O-80			I-33	44			94	I-73		
Common minus pin for +C/+D output ports	N-C/D	56	Common minus pin for +8/+9 output ports	6	N-8/9			I-34	45			95	I-74		
	N-C/D	55		5	N-8/9			I-35	46			96	I-75		
	N-C/D	54		4	N-8/9			I-36	47			97	I-76		
	N-C/D	53		3	N-8/9			I-37	48			98	I-77		
	N-C/D	52		2	N-8/9			Common plus pin for +2/+3 input ports	P-2/3	49		99	P-6/7	Common plus pin for +6/+7 input ports
	N-C/D	51		1	N-8/9				P-2/3	50		100	P-6/7	

* I-00 - I-17 can be used as interrupt signal.

Signal name	Description
I-00 - I-77	64 input signal pins. Connect output signals from the external device to these pins.
O-80 - O-F7	64 output signal pins. Connect these pins to the input signal pins of the external device.
P-0/1	Connect the positive side of the external power supply. These pins are common to 16 input signal pins.
P-2/3	Connect the positive side of the external power supply. These pins are common to 16 output signal pins.
P-4/5	Connect the positive side of the external power supply. These pins are common to 16 input signal pins.
P-6/7	Connect the positive side of the external power supply. These pins are common to 16 output signal pins.
P-8/9	Connect the positive side of the external power supply. These pins are common to 16 input signal pins.
P-A/B	Connect the positive side of the external power supply. These pins are common to 16 output signal pins.
P-C/D	Connect the positive side of the external power supply. These pins are common to 16 input signal pins.
P-E/F	Connect the positive side of the external power supply. These pins are common to 16 output signal pins.
N-8/9	Connect the negative side of the external power supply. These pins are common to 16 output signal pins. A current of 0.3 A is allowable per connector pin. Connect the required number of pins for the total current for the corresponding 16 output signals. All 6 pins must be connected when using all 16 outputs at the maximum rated output current of 100 mA.
N-A/B	Connect the negative side of the external power supply. These pins are common to 16 output signal pins. A current of 0.3 A is allowable per connector pin. Connect the required number of pins for the total current for the corresponding 16 output signals. All 6 pins must be connected when using all 16 outputs at the maximum rated output current of 100 mA.
N-C/D	Connect the negative side of the external power supply. These pins are common to 16 output signal pins. A current of 0.3 A is allowable per connector pin. Connect the required number of pins for the total current for the corresponding 16 output signals. All 6 pins must be connected when using all 16 outputs at the maximum rated output current of 100 mA.
N-E/F	Connect the negative side of the external power supply. These pins are common to 16 output signal pins. A current of 0.3 A is allowable per connector pin. Connect the required number of pins for the total current for the corresponding 16 output signals. All 6 pins must be connected when using all 16 outputs at the maximum rated output current of 100 mA.
N.C.	This pin is left unconnected.

⚠ **CAUTION**

To perform input/output using this product with the CONTEC device driver, specify logical ports and logical bits when calling each function. For details, refer to the "Relationships between API-TOOL Logical Ports/Bits and Connector Signal Pins" of Reference Manual.
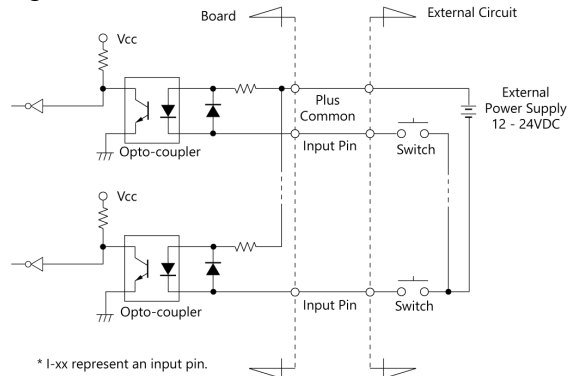
Connecting Input and Output Signals

Input Circuit

Connect the input signals to a device which can be current-driven, such as a switch or transistor output device.

The connection requires an external power supply to feed currents.

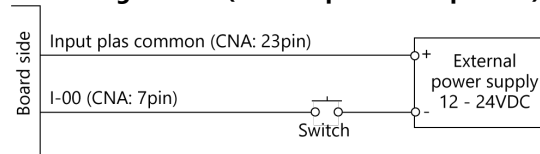The product inputs the ON/OFF state of the current-driven device as a digital value.

* I-xx represent an input pin.

The input circuits of interface blocks of the PIO-64/64L(PCI)H are illustrated in the figure above.

The signal inputs are isolated by opto-couplers (ready to accept current sinking output signals). The product therefore requires an external power supply to drive the inputs. The power requirement for each input pin is about 5.1 mA at 24 VDC (about 2.6 mA at 12 VDC).

### Connecting a Switch (An Example to use Input I-00)

When the switch is ON, the corresponding bit contains 1.
When the switch is OFF, by contrast, the bit contains 0.

Output Circuit

Connect the output signals to a current-driven controlled device such as a relay or LED.

The connection requires an external power supply to feed currents.

The product controls turning on/off the current-driven controlled device using a digital value.
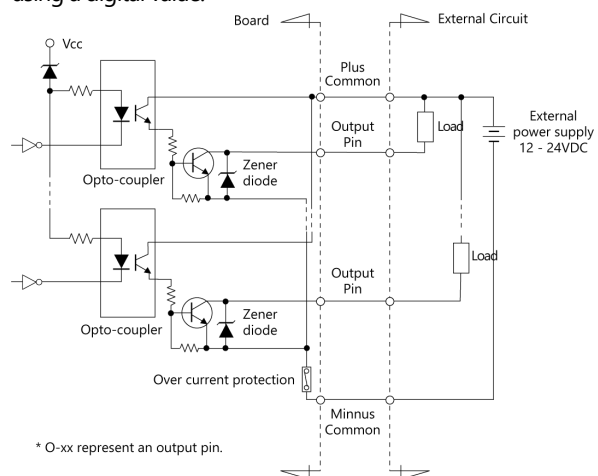
* O-xx represent an output pin.

The output circuits of interface blocks of the PIO-64/64L(PCI)H are illustrated in the figure above.

The signal output section is an opto-coupler isolated, open-collector output (current sink type).

The rated output current per channel is 100mA at maximum. The output section can also be connected to a TTL level input as it uses a low-saturated transistor for output.

The residual voltage (low-level voltage) between the collector and emitter with the output on is 0.5V

or less at an output current within 50mA or at most 1.0V at an output current within 100mA.
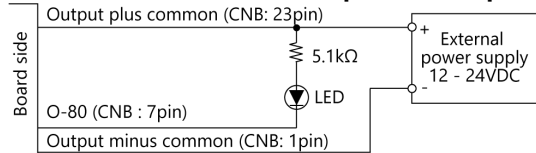
A zener diode is connected to the output transistor for protection from surge voltages.

An overcurrent protection component is provided for every 8 output transistors.
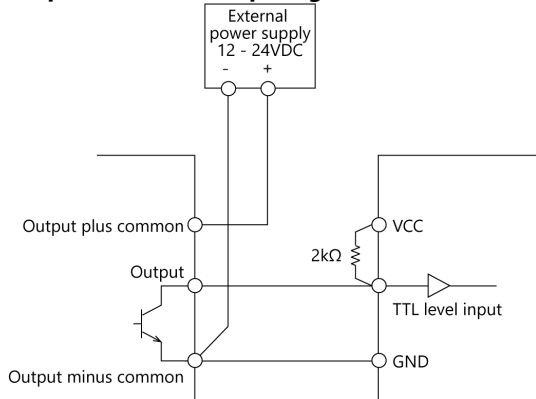
⚠ **CAUTION**

When the PC is turned on, all outputs are reset to OFF.

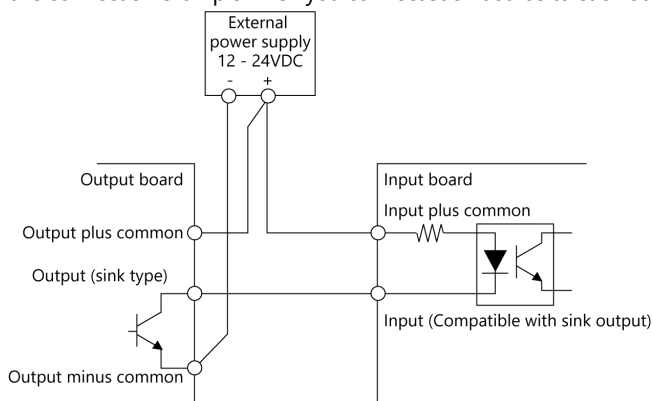### Connection to the LED (An Example to use Output O-80)

When "1" is output to a relevant bit, the corresponding LED comes on.
When "0" is output to the bit, in contrast, the LED goes out.

Example of Connection to TTL Level Input (Connection Example of Output and TTL level Input Signal)

Connecting the Sink Type Output and Sink Output Support Input

The following example shows a connection between a sink type output (output board) and a sink output support input (input board). Refer to this connection example when you connect such boards to each other.

Block Diagram